



Shanmugapriya.M

Department of Mechatronics

KCG College of Technology

Chennai-600097

Teaching Experience-

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OBJECTIVE:

1. Working as Assistant Professor in **KCG college of Technology Chennai**, From 18.6.18 to Till date
2. Worked as a **Lecturer in Prince Sri Venkateswara Padmavathy Engineering College, Chennai**, Jun 2011-Dec 2012.
3. Worked as a **Sr. DSP Software Engineer** in **Sobha Applied DSP Pvt. Ltd., Chennai** (Joint venture of SRIT-Bangalore)
4. Worked as a **R&D Engineer Trainee** in **Numeric Power Systems Pvt. Ltd., Chennai**. Aug 2005-Oct 2006.

PAPERS PUBLISHED IN SCOPUS JOURNAL:

1. **“Power Management of a Microgrid using Fuzzy Logic Controller”**, published in International Journal for Advance Science and Technology.
2. **“Design and Analysis of Hybrid Optimization Technique for Maximum Power Tracking in PV under Uniform and Partial Shading Condition”**, published in International Journal for Advance Science and Technology.
3. **Comparative Study of Firefly Algorithm with PI, PID, FUZZY Logic Controller for Diode Bridge Power Factor Corrector**, Published in International Journal of Pure and Applied Mathematics, Volume 120 No.6 2018, 1129-1140
4. **An Energy Conservation Opportunity in Paper & Pulp Industry**, Published in Journal of Green Engineering, Vol.9 475-488
5. Shanmugapriya M “Modeling of Voltage Droop Control in DC Microgrid for Voltage Profile Improvement” (Paper ID: ICIHCNN-1439) Advanced Technologies and societal change-ISSN-2191-6853, Proceedings of ICIHCNN2022 (Book Series Springer)

PAPERS PUBLISHED IN INTERNATIONAL CONFERENCE:

1. M. Shanmugapriya "Size Optimization Algorithm for a Hybrid Microgrid system" 2nd International Conference on Recent Trends In Engineering Technology and Management 2022 organised by Christ The King Engineering College in Association with Organization of Science & Innovative Engineering and Technology (OSIET), Chennai—Awarded as Best Paper 2023
2. **"Power Management of a Microgrid using Fuzzy Logic Controller"**, published in International Conference on Integration of Advanced Technologies for Industry 4.0 (ICIATI), Sponsored by AICTE conducted in KCG College of Technology, Chennai.
3. **"Design and Analysis of Hybrid Optimization Technique for Maximum Power Tracking in PV under Uniform and Partial Shading Condition"**, published in International Conference on Integration of Advanced Technologies for Industry 4.0 (ICIATI), Sponsored by AICTE conducted in KCG College of Technology, Chennai.
4. **Slide Mode Based PID Controller for DC/DC converter** paper published in International Conference conducted by PERI Institute of Technology Chennai.
5. **Self Powered Railway Track Monitoring Using IoT** paper published in International Conference Conducted by Eswari Engineering College, Chennai.
6. **International Conference Systemics, Cybernetics and Informatics (ICSCI-2005)** Conducted by Pentagram Research Centre at Hyderabad.
7. **"Cavalier Dr. G.K. Deverajulu Technological Excellence Competition"** on account of the National Technology Day Celebrations conducted by Lakshmi Machine Works Limited (LMW) at Coimbatore.

FACULTY DEVELOPMENT PROGRAM:

1. Attended FDP on **"Artificial Intelligence"** conducted by Indra Prastha Institute of Information Technology, Delhi (IIIT-Delhi), sponsored by AICTE Training and Learning (ATAL) Academy.
2. Attended FDP on **"Fundamentals of Machine Learning"** conducted by ICT Academy, Coimbatore.
3. Attended FDP on **"Online Teaching Using MSTEAMS"** conducted by Microsoft and KCG College of Technology, Chennai.
4. Attended FDP on **"Two Day Hands-on-Training on Solving Multi objective Optimization Problems Using Deterministic & Stochastic Algorithms (Through Online Mode- V2.0)"** conducted by CIT, Coimbatore.
5. Attended FDP on **"TEACHING LEARNING CENTRE IIT MADRAS"** Active Learning Methods conducted by IIT Madras, Chennai.
6. Attended FDP on **"Design of Renewable Energy Systems"** conducted by ANNA UNIVERSITY, Chennai.
7. Attended FDP on **"Soft-Skill"** conducted by KCG College of Technology, Chennai.
8. Attended FDP on **"Analysis of power protection and automation"**

currentscenario''conductedbyKCGCollegeofTechnology, Chennai, Sponsored byAICTE.

TRAININGPROGRAM/SEMINAR:

1. Ministry of New and Renewable Energy (MNRE) and IIT Bombay Sponsored One day Hands on Training toassembleSolarStudyLampconducted at KCGcollegeofTechnologyChennai.
2. MSME certified “Industry 4.0” Smart Manufacturing Workshop conducted by St.Joseph’s Institute ofTechnology.
3. AttendedSeminaronElectroStaticDischarge(ESD)-
GeneralAwarenessandIntroductionconductedbyKCGcollegeofTechnology
4. Attended“**PEERBasicWorkshop**“conductedat**KCGCollegeofTechnology**.
- 5.

AWARDS:

1. Got Appreciation Award from **DST & Texas Instruments** for extensive support to students projects initiatedbyTexas.

ONLINE COURSES:

1. Completed“IntroductiontoElectronics”,ConductedbyCOUSERA
2. Completed“PositivePsychology”,ConductedbyCOUSERA
3. Completed“MachineLearning”,ConductedbyTATASTEEL
4. Attended“**FundamentalofElectricalEngineering**”,ANPTELCourseconductedby**IITMadras**,
whichisequivalenttoFacultyDevelopmentProgram.
5. Attended“**DigitalSignalProcessing**” ANPTELCourseconductedby**IIT**.
6. Attended“**Hands-ontrainingonSolarStudyLampAssembly**”conductedby**IIT-BombayX**.

PROFESSIONALMEMBERSHIP:

- 1.LifeTimemembershipCategoryin“InternationalAssociationofEngineers(IAENG)”Membershipno.
217080

PROJECTGUIDED:

1. DesignandDevelopmentof GateDriverCircuitusing**AdurinoandPICMicrocontroller**.
2. DesignandDevelopmentofDualActiveBridgeusing**TMS320C28069FPICOL**Microcontroller.
3. SlideModeBased**PIDcontrollerwithFuzzyLogicController** forDC/DCconverter
4. Self-PoweredRailwayTrackMonitoring Using**IoT**.
5. Controllerdesignusing**OptimizationAlgorithm(PSA,FFAetc...)**

SUBJECTSHANDLED:

Theory Courses	Laboratory Courses
<ul style="list-style-type: none"> • Digital Signal Processing • Signal and Systems • Measurement and Instrumentation • Linear Integrated circuits and Applications • Basic Electrical and Instrumentation Engineering • Electron Devices • Electric Circuits • Power Electronics and Drives 	<ul style="list-style-type: none"> • Linear and Digital Integrated Circuits Laboratory • Electron Devices Laboratory • Control and Instrumentation Laboratory • Basic Electrical Electronics and Instrumentation Engineering Lab

RESPONSIBILITIES HELD:

1. Department Research Co-Ordinator IQAC Co-Ordinator, Class Advisor, Library In charge, Amenities In charge, Department Lab Manual Controlled Copy In charge, Syllabus coverage In charge.
2. Coordinator for SIEMENS Training on AC/DC Drives-“SITRAIN”
3. Coordinator for “International Conference and Journal Publication for SCOPUS INDEX”.

ACADEMIC CHRONICLE:

1. Pursing PhD (Part Time- Completing stage) in **Kumaraguru College of Technology, Coimbatore, Anna University.**
2. M.E. degree in Power Electronics and Drives from Bannari Amman Institute of Technology, Anna University in 2005
3. B.E. in Electrical and Electronics Engineering from Bannari Amman Institute of Technology, Bharathiyar University in 2003