

VERTICAL 1: SEMICONDUCTOR CHIP DESIGN AND TESTING

SL. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PERIODS PER WEEK			TOTAL CONTACT PERIODS	CREDITS
				L	T	P		
1	23EC031	Advanced Digital System Design	DEC	3	0	0	3	3
2	23EC032	Analog IC Design	DEC	2	0	2	3	3
3	23EC033	Low Power IC Design	DEC	2	0	2	3	3
4	23EC034	VLSI Testing and Design For Testability	DEC	2	0	2	3	3
5	23EC035	Physical Design	DEC	3	0	0	3	3
6	23EC036	Mixed Signal IC Design and Testing	DEC	3	0	0	3	3